

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO. 09/872955	CONT/PRIOR F	CLASS 324 316	SUBCLASS 4	ART UNIT 2858	EXAMINER Rossoshek
------------------------------	-----------------	--------------------------------	---------------	-----------------------------	-----------------------

APPLICANTS

Kenji Araki
Ayao Yokoyama

2825

TITLE

System for checking wiring configuration of printed circuit board

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

☐ **TERMINAL
DISCLAIMER**

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____

☐ The terminal months of this patent have been disclaimed.

DRAWINGS

Sheets Drwg.

Figs. Drwg.

Print Fig.

(Assistant Examiner)

(Date)

(Primary Examiner)

(Date)

(Legal Instruments Examiner)

(Date _____)

CLAIMS ALLOWED

Total Claims

Print Claim for O.G.

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due

Date Paid

ISSUE BATCH NUMBER

WARNING:

WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form **PTO-438A**
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)

BEST AVAILABLE COPY

(FACE)